

# Parylene Coated Silicon Probes for Neural Prosthesis

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**Abstract**— Silicon neural prosthetic probes require reliable sensing electrodes able to access deep cortical structures without breakage. However, manufacturing limitations have prevented a strong and biocompatible silicon electrode array from reaching this goal. We here demonstrate the first high-density, parylene-coated silicon probe (1.2 cm long) with micro-fabricated electrodes that is able to be inserted in vivo without failure. This work also presents new experimental results for array shank deflection testing, lifetime soak testing as well as the in vitro electrical characterization of the gold and platinum micro-electrodes. These results allow us to optimize the geometry and treatment for both the silicon probe and the metal electrodes.

**Keywords** - parylene cable; neural prosthesis; silicon probe

## I. INTRODUCTION

An important component of silicon neural prosthetic is an electrode array capable of recording neural activity from the cortex. Such an array should be biocompatible and possess both strong mechanical properties to overcome insertion forces and the ability to accommodate a high number of electrodes with optimal recording characteristics. However, strong mechanical properties and higher electrode densities are often associated with larger geometries which may cause more cortical damage during insertion. Thus, a biocompatible platform that balances the size and signal acquisition capability tradeoffs must be studied and developed.

In this work, we present a total parylene coated silicon probe that has the potential to overcome these difficulties. Two major improvements of the current device over its predecessor [1] are the complete encapsulation of the array with parylene and the smoothing of the sidewalls of the silicon shanks by meniscus etching. Our previous generation consisted of a silicon substrate that was exposed on all sides except the front side, significantly reducing the biocompatibility of the device. Aside from biocompatibility concerns, an untreated array could be more prone to breakage due to surface defects created during deep reactive ion etching (DRIE). As before, we have chosen to use parylene C (poly-paraxylylene C) due to its insulation capability, flexibility, biocompatibility, and its prior use in medical applications.

Regarding recording capability, an ideal metal electrode for neural prosthetics application would have low impedance and high spatial specificity with a high signal-to-noise ratio (SNR). However, low impedance and high specificity are competing attributes as low impedances allow the electrode to pick up a

larger number of neurons. Also, impedance and area, and thus specificity, are inversely correlated, making it difficult to optimize both SNR and specificity.

This work further introduces the characterization of platinum and gold micro-electrodes. The electrode's impedance dependency on the size of the plasma-etch-opened metal electrodes is presented, which will allow us to determine the optimum material and size of the electrode for neuron specificity testing as well as future electrode optimization for primate prosthesis recording.

As a result, we are able to fabricate silicon probe devices that are well characterized, fully biocompatible, and easy to mass fabricate which will facilitate future research in developing a neural prosthetic system.

## II. DEVICE DESIGN

A flexible parylene lift-off technology [2] allows us to fabricate 2-D 32 channel flexible cabled electrode array devices. It is a major feature of our device that the cabling is not a component that is later attached to the array but that it is an integrated extension of the top parylene structure of the array itself. These devices can be expanded to 3-D 32N channel structures by probe stacking N number of 2D probes. Figure 1 shows an unpackaged cabled silicon probe. The circular platinum rings arranged in a 60° Y shape pattern on the end of device are used to electrically bond to commercial available connectors with conductive epoxy on a circular PC board.

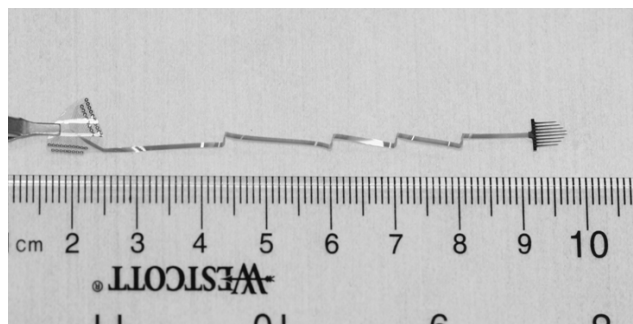


Figure 1. Released silicon probe with parylene flexible cable designed for rat in vivo acute testing. Cable length is approximately 7 cm and the silicon shank length is approximately 5 mm.

In order to test the mechanical properties of the new silicon probes in vitro and in vivo, test probes are fabricated (Figure 2). The shank lengths range from 10 mm to 12 mm and are designed to reach deep cortical regions. Shanks are 150  $\mu$ m

*This project was funded by the Defense Advanced Research Projects Agency (DARPA) (Award # 908165) and by National Institute of Health (NIH) moveable probe project (Award # R01EY015545)*

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thick and 60  $\mu\text{m}$  wide towards the tip of the shank, which forms a  $10^\circ$  angle to facilitate insertion into the brain.

Impedance to area mapping of metal electrodes is essential for the optimization of the recording sites. An electrode only flexible parylene cable device is designed for this purpose (Figure 3). Both gold and platinum were used to fabricate 30 electrodes on each device with fifteen different areas that range from  $32 \mu\text{m}^2$  to  $3400 \mu\text{m}^2$ . Each of the electrodes has a side wall height of 6.5  $\mu\text{m}$ .

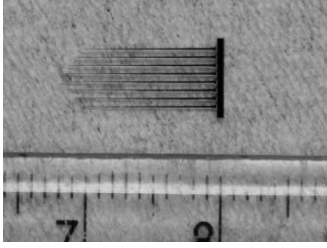


Figure 2. 1.2 cm silicon probe shanks designed specifically to reach human / primate deep cortical structures. It is used in this work for in vitro bending and in vivo insertion experimentation.

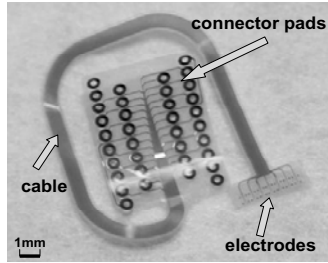


Figure 3. Testing device for electrode impedance mapping experiment. There is no silicon shank attached at the bottom of the electrodes

### III. FABRICATION

The high density silicon probes are fabricated on double side polished wafers with DRIE technology. Figure 4 shows the fabrication process [1]. First, 0.5  $\mu\text{m}$  of parylene C is deposited on one side of the double side wafer and is melted in an oven at  $350^\circ\text{C}$ , which is then patterned by oxygen plasma to leave areas for adhesion enhancement between the parylene / silicon interface. A sacrificial photoresist layer of 1  $\mu\text{m}$  thickness is then coated to facilitate the final release of the device. A bottom layer of parylene C (6.5  $\mu\text{m}$ ) is then deposited. This is followed by a layer of lift-off electron-beam evaporated platinum (0.25  $\mu\text{m}$ ) to define the trace lines, the electrodes and connector pads. The top layer of parylene C (6.5  $\mu\text{m}$ ) is deposited to complete the parylene-metal-parylene sandwich structure. Electrode sites and the device definition are then etched by a two step reactive ion etching (RIE) with  $\text{O}_2$  Plasma process. Silicon probe shanks are subsequently defined and etched by DRIE from both sides of the wafer. The bottom DRIE etching duration defines the thickness of the probe; by depositing and painting protective photoresist die by die, the thickness of every probe on the wafer can be controlled precisely. The devices are then released in

photoresist stripper and polished by meniscus etching [3], which is performed by submersing the probe in 1:19 /  $\text{HF}:\text{HNO}_3$  silicon etchant and gradually pulling it out with a micro-manipulator (Figure 5) at approximately 1 mm per minute. The result of the etching is shown in figure 6.

The silicon probes and cable are then totally coated on all sides. This is done by covering the connector pads with hand-painted photoresist and conformal coating 1  $\mu\text{m}$  of parylene C over the entire device. The device is covered with photoresist again to protect the cables and the rest of the device, leaving the silicon probe section exposed. This structure is then etched directionally with RIE to re-open the parylene coated electrodes (Figure 7). This device is later released in acetone and photoresist stripper solution. The silicon probe was dipped into a silicon etchant ( $14\text{CH}_3\text{COOH} + 10\text{HF} + 5\text{HNO}_3$  [4]) after the RIE etching to test for total coating. The silicon was not etched after 1 minute of dipping, which validates the coating is still intact and thus full biocompatibility is achieved. (Figure 8) Finally, the device is annealed at  $200^\circ\text{C}$  for two days to bond the sandwiching parylene layers.

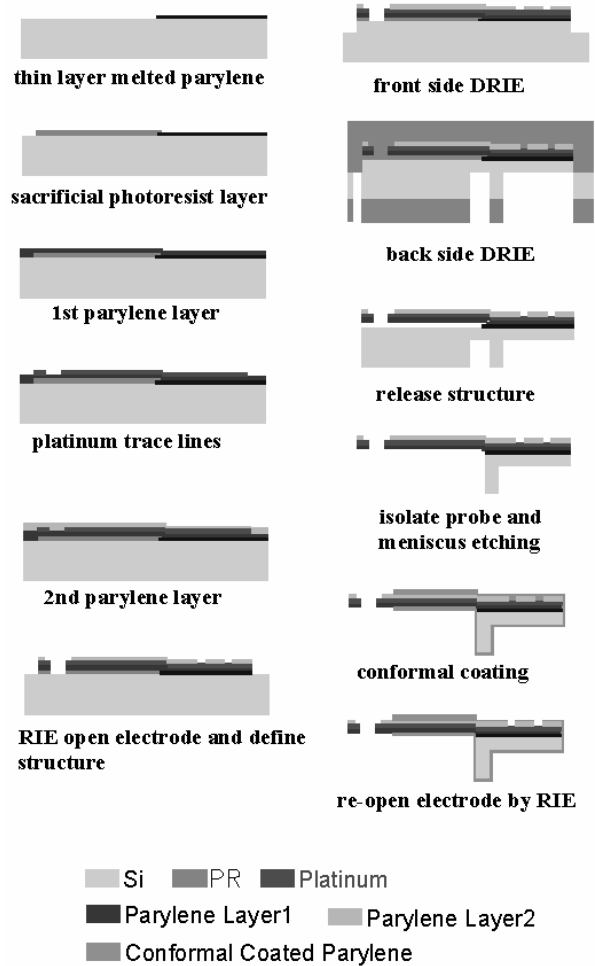


Figure 4. Fabrication process steps of the complete device



Figure 5. Meniscus etching setup. The probe-only device is pulled out of the silicon etchant by a manipulator located on the left hand side of the picture.

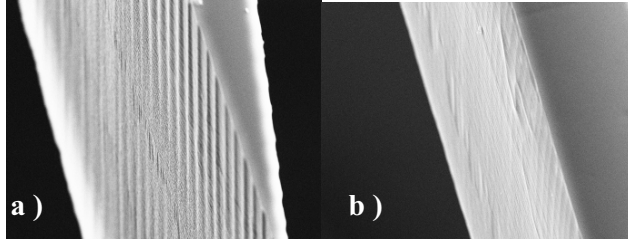


Figure 6. a) Silicon probe before meniscus-etching b) silicon probe after meniscus-etching. The etching rate is estimated to be around 7  $\mu\text{m}$  / minute.

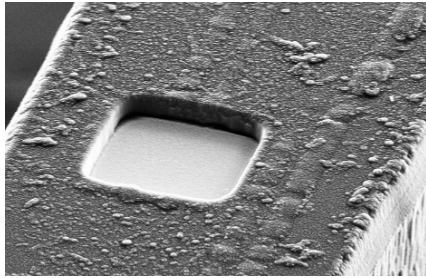


Figure 7. Opened electrodes after final RIE etching

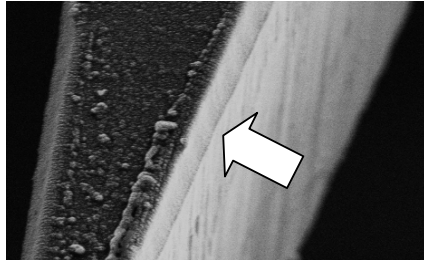


Figure 8. Silicon probe edge after final RIE etching. The edge is still sealed after etching.

#### IV. RESULT AND DISCUSSION

Device testing was performed in four phases: mechanical testing of the silicon shanks, in-vivo probe penetration testing, life time testing of the silicon probes and metal electrode characterization.

In the first phase, we used the set up in (Figure 9) to conduct the mechanical deflection testing of the shank only devices. Four types of sample with four devices each ( $N = 4$ ) were used to observe the effect of each treatment. Table 1

shows that meniscus-etching and parylene coating both improved the strength of the arrays for all bending directions. A parylene coated, meniscus-etched shank with length 1.2 cm is able to safely deflect around 5.4 mm from top bending and more than 3.8 mm from side bending before breaking (Figure 10). Shanks are also consistently breaking further away from the tip after treatment showing the elimination of defects along the shanks. This improvement both reduces the chance of breakage of the device during handling/insertion and reduces probe residues left in test subjects in case of breakage.

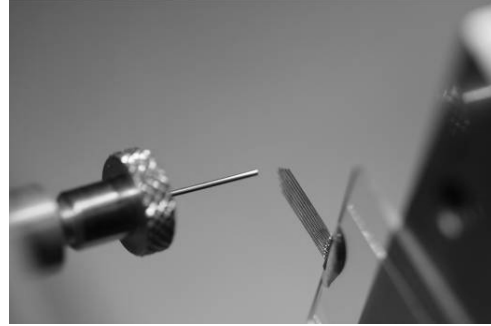


Figure 9. Silicon probe deflection test setup. The silicon probe (right hand side) is glued to a glass slide by epoxy and deflected by a metal rod attached to a force gauge.

TABLE I. SAMPLES WITH COMBINATION OF FACTORS AND TEST RESULT. SAMPLE 1 HAS 200  $\mu\text{m}$  BASE WIDTH AND THICKNESS OF 175  $\mu\text{m}$ . 4 DEVICES WERE TESTED ( $N = 4$ ) FOR EACH SAMPLE NUMBER

Sample Number	Parylene Total Coated	Sharpened	Top Deflection / Force	Top Breaking point (away from tip)
1 (controlled)			5.36mm / 71mN	10mm
2		x	5.69mm / 65mN	9.69mm
3	x		5.45mm / 70mN	11.56mm
4	x	x	5.40mm / 80mN	11.87mm
	Side Deflection / Force	Side Breaking point (away from tip)	Bottom Defl. / Force	Bottom Breaking point (away from tip)
1 (controlled)	3.05mm / 47mN	3.6mm	1.77mm / 34mN	8.6mm
2	3.10mm / 52mN	3.8mm	3.02mm / 61mN	8.95mm
3	3.54mm / 54mN	5.08mm	1.97mm / 30mN	11.69mm
4	3.81mm / 70mN	5.2mm	2.51mm / 49mN	11.44mm

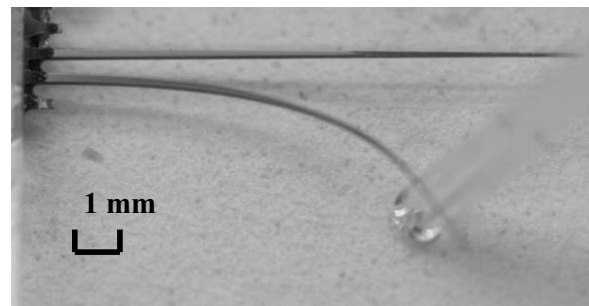


Figure 10. Silicon probe bending from the side by hand

Secondly, the devices were inserted through the pia and into the cortex of rats to test penetration ability. Results show a full

insertion of the probe was successful without any bending, buckling or breakage by observing the shanks under a surgical microscope during insertion. Cortical dimpling induced by the array during this in vivo insertion was estimated to be approximately 1 mm.

In the third phase, the devices underwent accelerated life testing in saline to determine the mean time to failure of the silicon to parylene adhesion. Soaking test results show that the parylene-silicon adhesion is able to withstand more than five weeks in 90°C saline (Figure 11) before delamination occurs.

Finally, device testing of the metal electrodes was conducted by submersing the electrode only structure in saline solution and measuring the impedance values at 1 kHz. We tested six devices (N = 6). It is observed that platinum electrodes have impedance values significantly lower than those of the gold electrodes, allowing higher specificity for the same electrode area (Figure 12). The result also shows a linear relationship in log-log scale plot of impedance vs. area and impedance vs. frequency. (Figure 13)

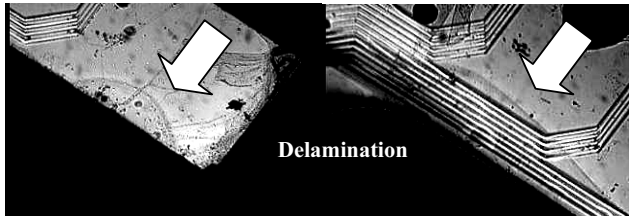


Figure 11. Delamination occurrence after five weeks of soaking in 90°C saline solution. The probe was still attached on the parylene cable after eight weeks of soaking.

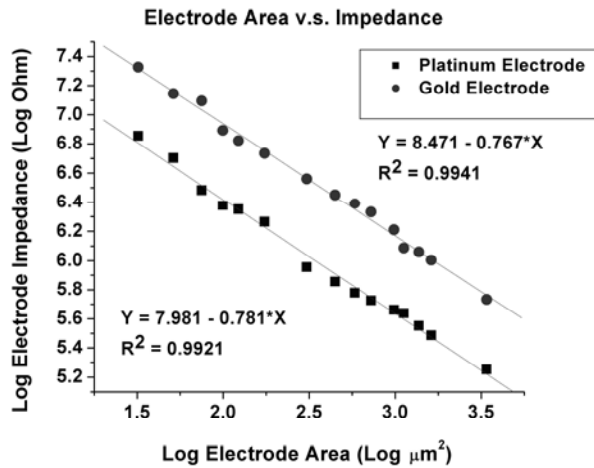


Figure 12. Log-log scale plot of gold and platinum electrode impedance mapping as a function of area measured at 1 kHz.

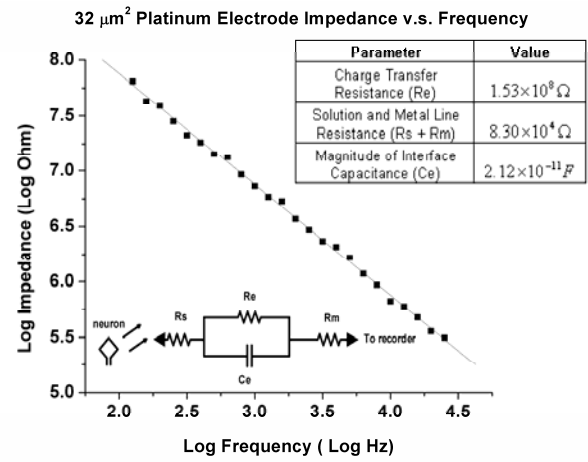


Figure 13. Log-log scale plot of impedance vs. frequency. The legend shows the fitted values of the components in the equivalent circuit model [5].

## V. CONCLUSION

We have developed and demonstrated the feasibility of a fully parylene C encapsulated, high electrode density, silicon based array with an integrated flexible cable that is able to access deep cortical structures. The result of the mechanical deflection test was presented and the in vivo insertion capability of the silicon probes has been verified. High temperature accelerated life time testing in saline indicates the stability of parylene-silicon interface for more than five weeks in 90°C saline. The area-impedance mapping of platinum and gold electrodes was completed, which will facilitate the optimization of the electrode recording performances. From the testing results, this device shows promise as a neural probe platform to be incorporated into a future chronic cortical prosthetic. Future work is underway to optimize the electrode placement, geometry and platinum black coating process for acute signal specificity testing.

## ACKNOWLEDGMENT

The authors would also like to thank Mr. Trevor Roper, other members of the Caltech Micromachining Laboratory and Andersen lab at Caltech for their advice and help.

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